.//	AMENDMENT TRANSMITTAL LETTER							
E JI	IN 2 1 2004 S		*	FRE	on lie		No: N18347102E	
TA.	Application Serial Number:			ling Date: 5/20/2003	Examiner: Tran, Tan N.		Group Art Unit: 2826	
Inve	Invention: Semiconductor Integrated Circuit and Method of Manufacturing the Same							
Tran	TO THE COMMISSIONER OF PATENTS AND TRADEMARKS: Transmitted herewith is an amendment in the above-identified application. The fee has been calculated as shown below.							
	CLAIMS AS AMENDED							
		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	NO. OF EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE	
TOTA	AL CLAIMS	20	MINUS	20	0	\$18	\$ 0.00	
INDE	P. CLAIMS	2	MINUS	3	0	\$86	\$ 0.00	
07/02/2002 12. 21 FC: 200	Petition is hereby made under 37 CFR 1.136(a) to extend the time for response to the Office Action of to and through comprising an extension of the shortened statutory period of: one month (\$110) three months (\$950) four months (\$1510) Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by Small entity statuent to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed A check in the amount of \$ 0.00 is attached Charge \$ to Deposit Account X							
	I hereby certify that this correspondence and all correspondence identified as accompanying this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313 on							